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Email: support@edac.net

http://www.edac.net

ENGINEERING CHANGE NOTICE

Originator:	Bo Byoung Jeon	Date:	November 16, 2023	Document Number	ECN20231116-00
Phone No.	(416) 754-3322	Part Number	A63-113-431P112	Drawing Revision	Rev.6
Email Address:	bjjeon@edac.net				
Department	ENGINEERING				

CHANGE TYPE

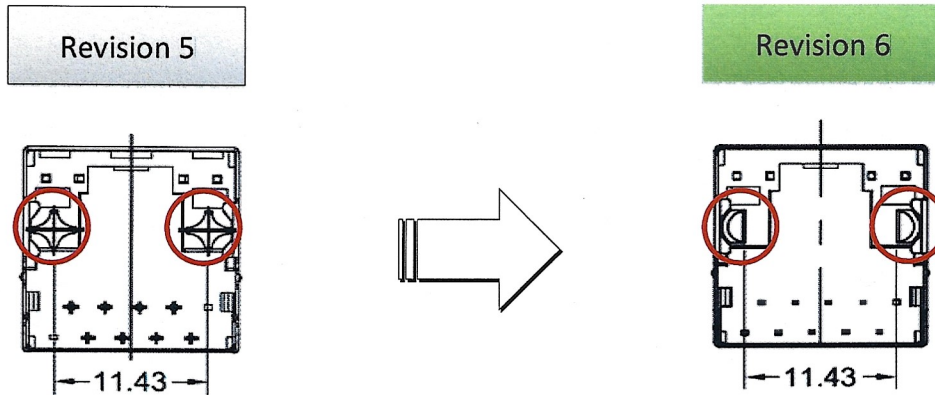
- CLASS I Customer notification and approval required prior to implementation
- CLASS II Customer notification only, no approval required
- CLASS III No customer notification required

REASON OF CHANGE

To improve product quality

DESCRIPTION OF CHANGE:

Modify the PEG design to improve product quality.



2. Effective implementation to distribution is on November 16, 2023

Note: N/A

PARTIES AFFECTED

- Customer
- Distributors
- Suppliers

- NORCOMP
- MH
- ETW

- ECA
- EDG
- EDAC UK

KEY TARGET DUE DATES IF CHANGE IS APPROVED TO PROCEED (check if applicable and show target dates as known)

- Submit Quote _____
- Prod. Trial Run _____
- Run at Rate _____

- PPAP from Supplier _____
- MRD of Production Parts _____

ACKNOWLEDGEMENT FOR ECN INITIATION: (OPTIONALS)

Tooling Rep	_____	Process Eng Rep	_____
Mfg Eng Rep	_____	Facilities Rep	_____
Production Rep	_____	Sales Rep.	_____
Materials Rep	_____	Product Eng. Rep.	Jacky Lai
Quality Rep	Bo Byoung Jeon	Purchasing Rep	_____

STATUS	
APPROVED	<input checked="" type="checkbox"/>
CCS CHANGE REQUEST #	_____

APPROVALS FOR ECN INITIATION (REQUIRED)

Vice President Bob Sakitkovski

Engineering Manager Ronnie Sta. Ronnie Sta.
 Monica Monica

Business Development Manager *K Samuels*
 Kobie Samuels 11/22/23

Quality Engineer Bo Byoung Jeon

Mechanical Engineer _____

Digitally signed by Bo Byoung Jeon
 DN: cn=Bo Byoung Jeon, o=EDAC, ou=Engineering Dept, email=bo@edac.net, c=CA
 Date: 2023.11.21 13:30:50 -0500

REJECTED

Change REJECTED by: _____

Rejected Date: _____

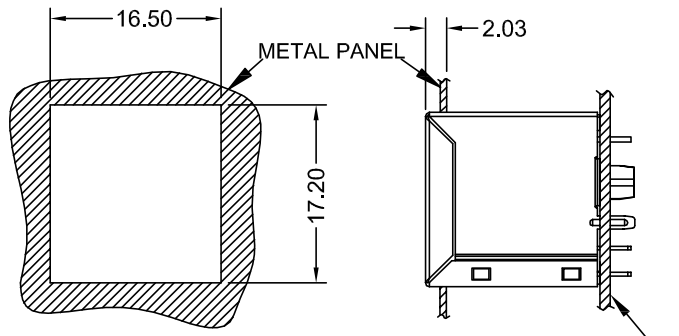
MINIMUM OF TWO SIGNATURES REQUIRED

UNLESS OTHERWISE STATED ALL DIMENSIONS ARE TOLERANCED TO $\pm 0.254[0.010]$

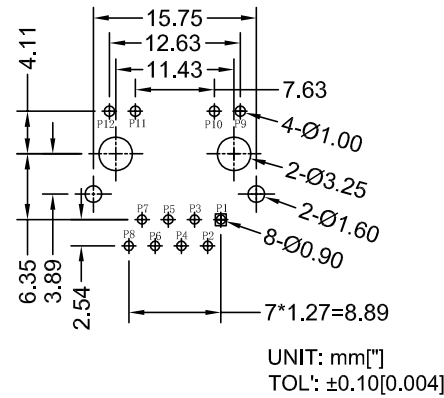
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ISSUE NUMBER	
ORIGINAL	①
DIM UPDATE-C.B	② 10.3.16
UPDATE DRAWING	③
Y.Z	01.04.2018
ADD CONTACT PLATING	④
N.B.	JUN.24/2021
UPDATE PEGS SHAPE	⑤
UPDATE ELEC. SCHEMATIC	
P.M.	NOV.12/2022
UPDATE PEGS SHAPE	⑥
J.L.	NOV.16/2023

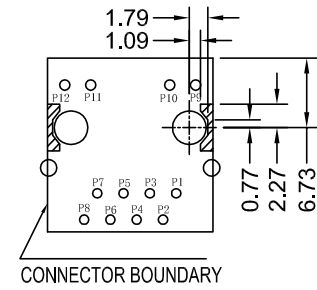


SUGGESTED PANEL OPENING

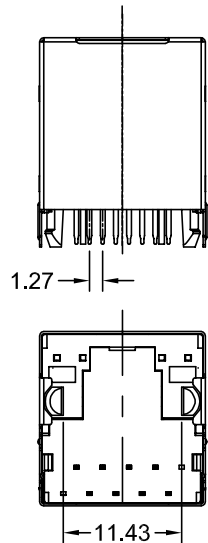
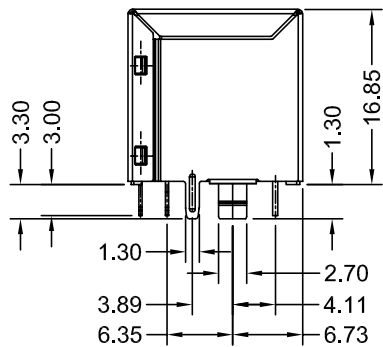
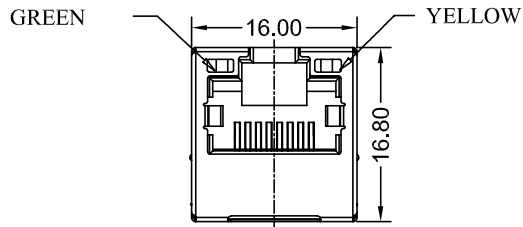


RECOMMENDED PCB LAYOUT

UNIT: mm["]
TOL: $\pm 0.10[0.004]$



CONNECTOR BOUNDARY



NOTES:

- CONNECTOR MATERIAL:
HOUSING: THERMOPLASTIC UL94 V-0
SHIELD: BRASS
SHIELD PLATING: NICKEL
CONTACT: COPPER ALLOY
CONTACT PLATING: GOLD PLATING IN CONTACT AREA

PART NO	GOLD PLATING THICKNESS
A63-112-431P112	GOLD FLASH PLATING
A63-113-431P112	15 MICRO INCHES OF GOLD
A63-114-431P112	30 MICRO INCHES OF GOLD

- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED
SEE ELECTRICAL DRAWING FOR OMITTED PINS
- RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68.
- THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS
PEAK SOLDERING TEMPERATURE IS 260°C MAX, 10 SECS MAX
- OPERATING TEMPERATURE T=0°C TO 85°C
- STORAGE TEMPERATURE T=-40°C TO 85°C



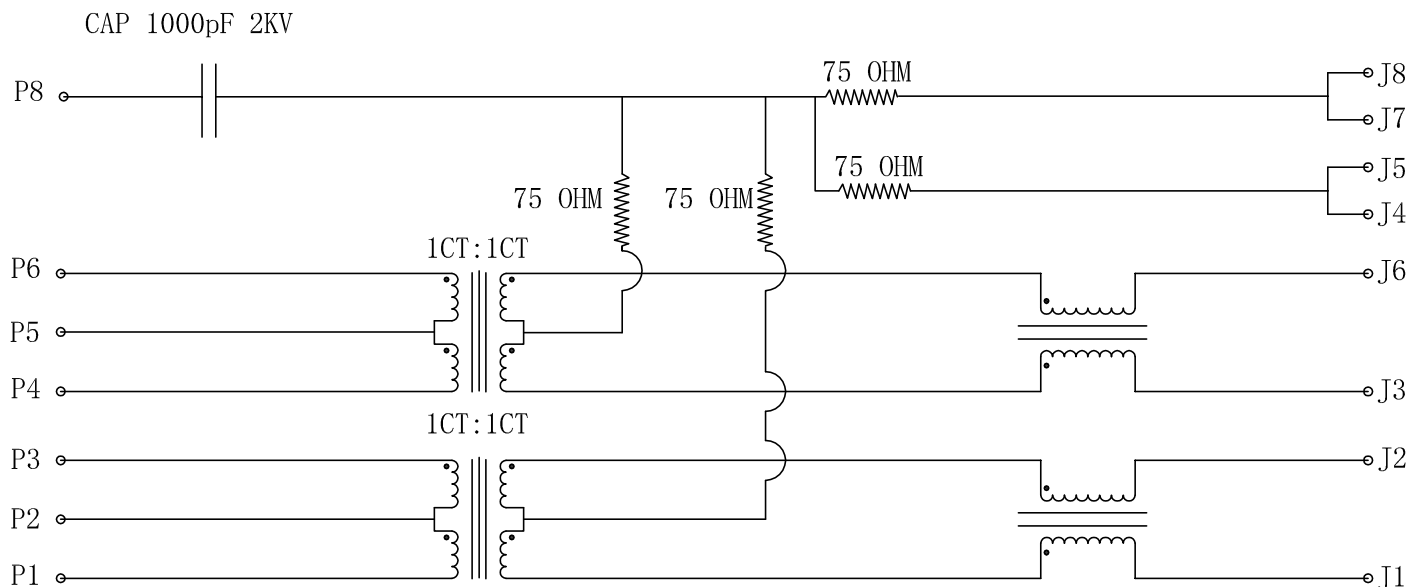
THIS SERIES FULLY CONFORMS TO THE EUROPEAN UNION DIRECTIVES 2011/65/EU FOR RoHS COMPLIANCY.

RJ45 MAGNETIC JACK WITH LED, VERTICAL, 8P, 8C SHIELDED, 10/100Mbps FILTER

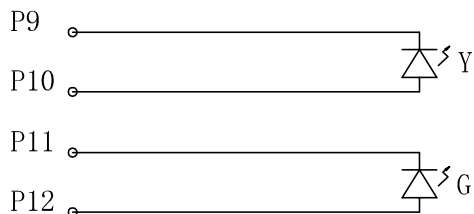
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ACAD REFERENCE NO. A63-113-431P112	
DRAWN: C.B	DATE: OCT 03/16
CHECKED: C.B.	DATE: NOV.16/2023
PART NUMBER: SEE NOTE	
DRAWING NUMBER A63-113-431P112	ISSUE 6



LED CIRCUIT:



LED SPECIFICATIONS (WITH FORWARD CURRENT OF 20 mA)			
STANDARD LED	WAVELENGTH	FORWARD V (MAX)	TYP
GREEN	565 nm	2.4 V	2.2 V
YELLOW	590 nm	2.5 V	2.1 V

PIN	YELLOW	PIN	GREEN
P9	-	P11	-
P10	+	P12	+

ELECTRICAL SPECIFICATIONS:

TURN RATIO @ 100KHz:	(P1~P3):(J1~J2) = 1:1±5%	(P4~P6):(J3~J6) = 1:1±5%
PRIMARY INDUCTANCE:	350µH min @ 100KHz, 0.1V 8mA DC BIAS	
DC RESISTANCE:	(J1~J2):(J3~J6)	1.2Ω MAX.
INSERTION LOSS:	1-100MHz	-1.2dB MAX.
RETURN LOSS:	1-30MHz	-16dB min.
	30-60MHz	-12dB min.
	60-80MHz	-10dB min.
CROSS TALK:	1-100MHz	-30dB min.
COMMON TO COMMON		
MODE ATTENUATION:	1-100MHz	-30dB min.
ISOLATION PHY SIDE TO LINE SIDE:	1500V AC OR 2250V AC	



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RJ45 MAGNETIC JACK WITH LED, VERTICAL, 8P, 8C
SHIELDED, 10/100Mbps FILTER

ACAD REFERENCE NO. A63-113-431P112

DRAWN: C.B.

DATE: OCT. 03/16

CHECKED: C.B.

DATE: NOV.16/2023

PART NUMBER:

SEE NOTE

SHEET 2 OF 2

DRAWING NUMBER

A63-113-431P112

ISSUE

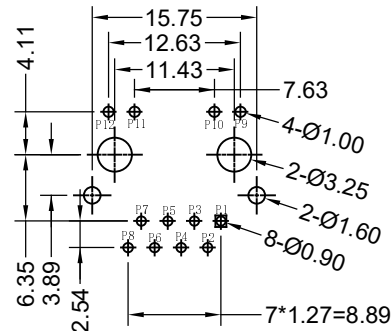
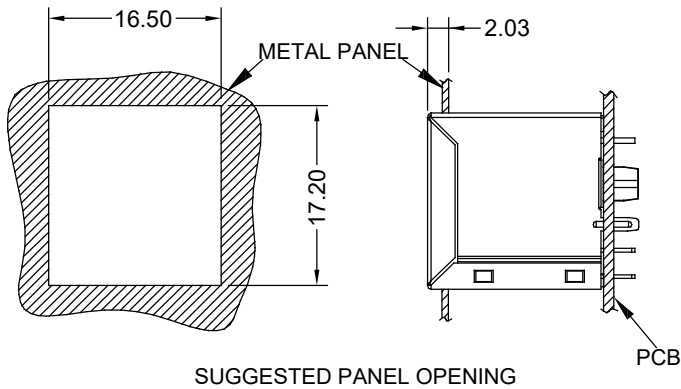
6

UNLESS OTHERWISE STATED ALL DIMENSIONS ARE TOLERANCED TO $\pm 0.254[0.010]$

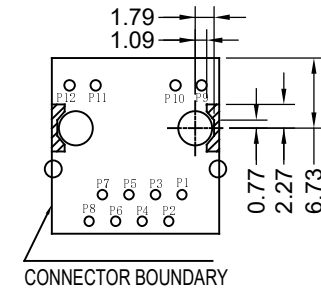
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DIM UPDATE-C.B 10.3.16	②
UPDATE DRAWING	③
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ADD CONTACT PLATING	④
N.B.	JUN.24/2021
UPDATE PEGS SHAPE	⑤
UPDATE ELEC. SCHEMATIC	
P.M.	NOV.12/2022



RECOMMENDED PCB LAYOUT

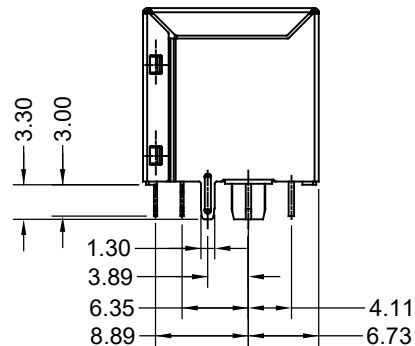
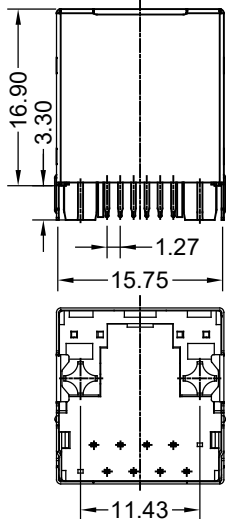
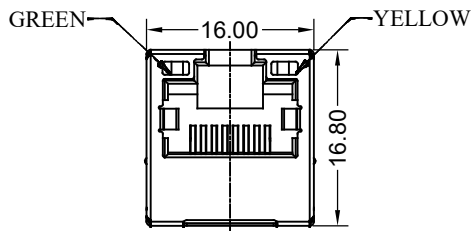


NOTES:

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- OPERATING TEMPERATURE T=0°C TO 70°C
- STORAGE TEMPERATURE T=-40°C TO 85°C
- ALL CRITICAL DIMENSIONS WITH "***"



RJ45 MAGNETIC JACK WITH LED, VERTICAL, 8P, 8C SHIELDED, 10/100Mbps FILTER

ACAD REFERENCE NO. A63-113-431P112

DRAWN: C.B DATE: OCT 03/16

CHECKED: N.B. DATE: JUN.24/2021

PART NUMBER:

SEE NOTE

SHEET 1 OF 2

DRAWING NUMBER
A63-113-431P112

ISSUE
5



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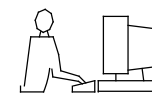
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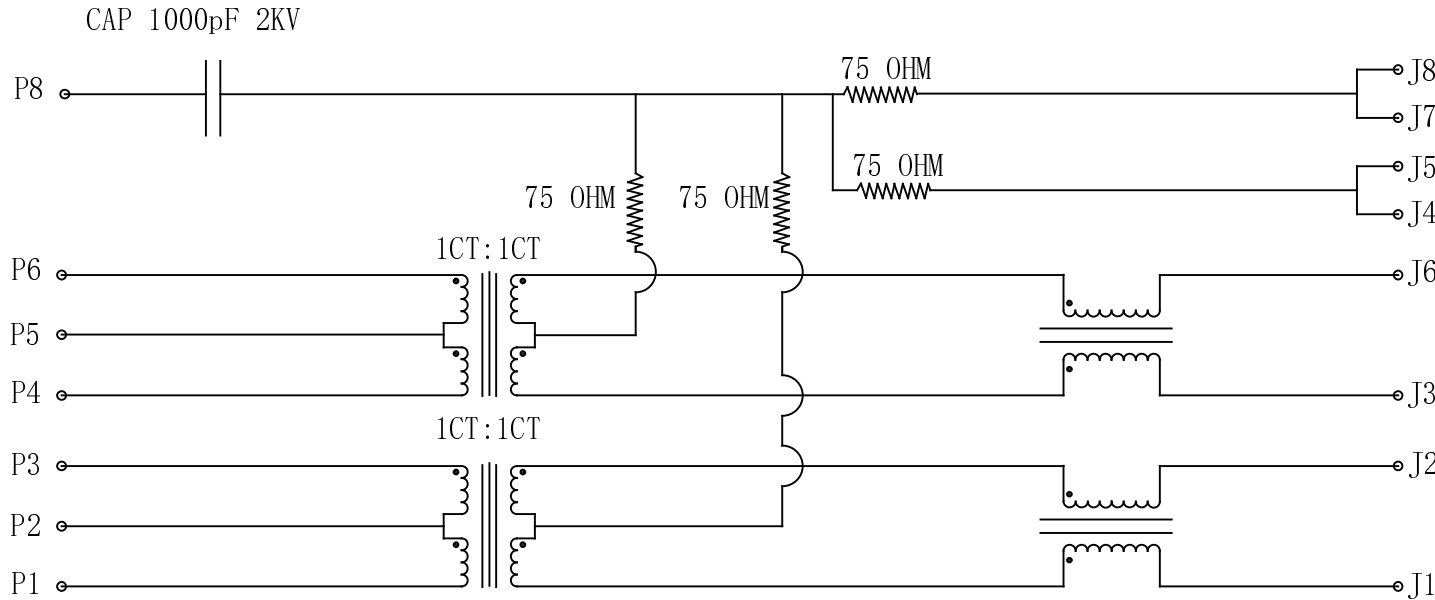
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CONNECTOR SOLDER SIDE

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0.1V 8mA DC BIAS

DC RESISTANCE: (J1~J2):(J3~J6) 1.2Ω MAX.

INSERTION LOSS: 1-100MHz -1.2dB MAX.
30-60MHz -12dB min.
60-80MHz -10dB min.

RETURN LOSS: 1-100MHz -30dB min.

CROSS TALK:
COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB min.

ISOLATION PHY SIDE TO LINE SIDE: 1500V AC OR 2250V AC

RJ45 MAGNETIC JACK WITH LED, VERTICAL, 8P, 8C
SHIELDED, 10/100Mbps FILTER

ACAD REFERENCE NO. A63-113-431P112	
DRAWN: C.B	DATE: OCT. 03/16
CHECKED: N.B.	DATE: JUN.24/2021
PART NUMBER: SEE NOTE	
DRAWING NUMBER A63-113-431P112	
ISSUE 5	



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